

U.S. PTO  
09/769534  
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Subclass	
Class	ISSUE CLASSIFICATION

TC5-555

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PATENT NUMBER

U.S. UTILITY Patent Application	
TR SCANNED	D.P.E. B O.A. AG.
PATENT DATE	

APPLICATION NO. 09/769534	CONT/PRIOR F	CLASS 327	SUBCLASS 143	ART UNIT 2816	EXAMINER England
Hideo Akayoshi					

SEMICONDUCTOR CIRCUIT ELEMENT AND METHOD FOR MANUFACTURING THE

PTO-2040  
12/99

### ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
INTERNATIONAL CLASSIFICATION			
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<input type="checkbox"/> TERMINAL DISCLAIMER		DRAWINGS		CLAIMS ALLOWED	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.		Sheets Drwg. (Assistant Examiner)	Figs. Drwg. (Date)	Print Fig. (Primary Examiner)	Total Claims (Date)
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				Print Claim for O.G. (Legal Instruments Examiner)	
NOTICE OF ALLOWANCE MAILED					
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		Amount Due (Date)		Date Paid (Date)	
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